

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	2	WinLC AND RTX	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/27 13:56
S2	3	WinLC	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/27 13:56
S3	15	slot ADJ plc	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/27 14:00
S4	43	soft ADJ plc	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/27 14:47
S5	186	719/327.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/28 14:36
S6	5	719/327.ccls. AND (PLC (programmable ADJ logic ADJ controller))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/27 14:57
S7	127	(PLC (programmable ADJ logic ADJ controller)) near3 driver	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/27 14:57
S8	7	((PLC (programmable ADJ logic ADJ controller)) near3 driver) SAME (configur\$4 manage\$3 install\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/28 11:37
S9	2	"6167425".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/28 13:10
S10	28	"5742825".pn. "5745759".pn. "5764984".pn. "5838968".pn. "5903752".pn. "5193189".pn. "5291614".pn. "5535380".pn. "5640563".pn. "5694604".pn. "5721922".pn. "5742825".pn. "5768599".pn. "5995745".pn. "6167425".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/28 13:17

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S11	36	"5742825".pn. "5745759".pn. "5764984".pn. "5838968".pn. "5903752".pn. "5193189".pn. "5291614".pn. "5535380".pn. "5640563".pn. "5694604".pn. "5721922".pn. "5742825".pn. "5768599".pn. "5995745".pn. "6167425".pn. US-5995745-\$.DID. OR US-6507861-\$.DID. OR US-4993017-\$.DID. OR US-6092095-\$.DID. OR US-5485579-\$.DID.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/28 14:07
S12	2692	(operat\$3 near3 ((real ADJ time) dual plurality)) SAME driver	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/28 14:09
S13	0	(operat\$3 near3 ((real ADJ time) dual plurality)) SAME driver SAME (re\$1assign\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/28 14:11
S14	0	(operat\$3 near3 ((real ADJ time) dual plurality)) SAME driver SAME (reassign\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/28 14:11
S15	177	(operat\$3 near3 ((real ADJ time) dual plurality)) SAME driver SAME (assign\$3 install\$5 allocat\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/28 14:12
S16	156	driver SAME reassign\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/28 14:13
S17	25	(device ADJ driver) SAME reassign\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/28 14:18
S18	54	(driver) near3 reassign\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/28 14:18
S19	50	719/327.ccls. AND virtual	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/28 14:36
S20	42	719/327.ccls. AND virtual AND (\$3assign\$4 install\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/29 10:09

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S21	4667	((plurality dual multi\$3) NEAR2 (operating kernel\$1)) AND virtual AND (\$3assign\$4 install\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/29 10:11
S22	30	((plurality dual multi\$3) NEAR2 (operating kernel\$1)) SAME (driver NEAR3 (\$3assign\$4 install\$5))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/29 10:11
S23	7	((plurality dual multi\$3) NEAR2 (operating kernel\$1)) SAME (driver NEAR3 (\$3assign\$4 install\$5)) AND virtual	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/29 10:53
S24	23	S23 xor S22	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/29 10:15
S25	11	(real ADJ time ADJ environment) SAME driver	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/29 11:03
S26	1060	(real ADJ time ADJ environment)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/29 11:03
S27	12	(real ADJ time ADJ environment) AND reassign	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/29 11:05
S28	98	(resource driver slot device) near2 reassign	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/29 11:06
S29	294549	((resource driver slot device) near2 reassign) (real ADJ time)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/29 11:06
S30	27	((resource driver slot device) near2 reassign) AND (real ADJ time)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/29 11:13
S31	1	(FPGA PLD PLC PLA) SAME (device driver resource slot) SAME reassign	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/29 11:14
S32	22036	(FPGA PLD PLC PLA) SAME (device driver resource slot)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/29 11:14

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S33	5357	(FPGA PLD PLC PLA) SAME (device driver resource slot) SAME (multiple plural\$3 several dual)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/29 11:15
S34	124	(FPGA PLD PLC PLA) SAME (device driver resource slot) SAME (multiple plural\$3 several dual) SAME (real ADJ time)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/29 11:15
S35	60	(FPGA PLD PLC PLA) SAME (device driver resource slot) SAME (multiple plural\$3 several dual) SAME (real ADJ time) SAME (operat\$3 OS environment)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/29 11:16
S36	212	((user kernel) ADJ mode) SAME (driver resource slot) SAME (\$3assign\$4 \$3install\$5 \$3configurat\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/30 15:37
S37	11	((user kernel) ADJ mode) SAME (driver resource slot) SAME (\$3assign\$4 \$3install\$5 \$3configurat\$3) AND (PLC PLA PLD FPGA ASIC)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/30 15:26
S38	148	((user) ADJ mode) SAME (driver resource slot) SAME (\$3assign\$4 \$3install\$5 \$3configurat\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/30 15:36
S39	120	((user kernel) ADJ mode) SAME (driver resource slot) SAME (\$3assign\$4 \$3install\$5 \$3configurat\$3) AND virtual	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/30 15:37
S40	89	((user) ADJ mode) SAME (driver resource slot) SAME (\$3assign\$4 \$3install\$5 \$3configurat\$3) AND virtual	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/31 14:48
S41	0	"10667060"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/31 14:48
S42	386	PLC SAME interface SAME operating	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/31 14:48
S43	44	soft ADJ (PLC PLD FPGA)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/04/04 11:40

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S44	1092	718/104.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/04/04 11:41
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Peter J. Denning
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15 Software-Only Bus Encoding Techniques for an Embedded System

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16 The role graph model and conflict of interest



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Peter Druschel
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18 Practitioner reports: Using predicate fields in a highly flexible industrial control system



Shay Artzi, Michael D. Ernst
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Christian Plattner, Gustavo Alonso
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20 A SMART scheduler for multimedia applications



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